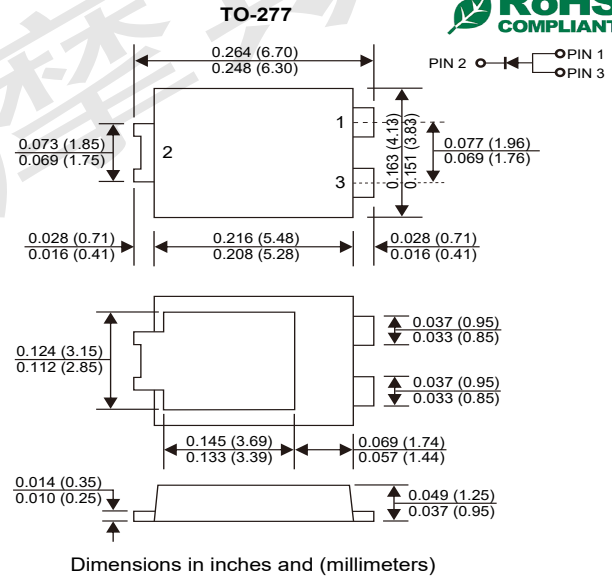


Features And Mechanical Data

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Metal silicon junction ,majority carrier conduction
- Guard ring for over voltage protection
- Low power loss ,high efficiency
- High current capability ,Low forward voltage drop High surge capability
- For use in low voltage ,high frequency inverters, free wheeling ,and polarity protection applications
- High temperature soldering:260°C MAX at 10 seconds, per JESD 22-B106 or MIL-STD-750,method 2026
- Case: JEDEC TO-277 molded plastic body



Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60HZ, resistive or inductive load. For capacitive load, derate current by 20%.

Type Numbe		Symbols	SR15200SP-TFH-L			Units
Device Marking			SS15200			
Maximum Recurrent Peak Reverse Voltage		V _{RRM}	200			Volts
Maximum RMS Voltage		V _{RMS}	140			Volts
Maximum DC Blocking Voltage		V _{DC}	200			Volts
Maximum average forward rectified current (see Fig.1)		I _(AV)	15.0			Amp
Peak Forward Surge Current, 8.3 ms single half-sine-wave superimposed on rated load (JEDEC method) (Rating of per diode)		I _{FSM}	300			Amp
Maximum instantaneous forward voltage at I _(AV) (Note 1)		V _F	Min	Typ	Max	Volts
	1A@ T _J =25°C		-	-	-	
	3A@ T _J =25°C		-	-	-	
	15A@ T _A =25°C		-	0.75	0.95	
	1A@ T _J =125°C		-	-	-	
	3A@ T _J =125°C		-	-	-	
	15A@ T _J =125°C		-	0.7	-	
Maximum Reverse Current at Rated DC Blocking Voltage(Note 2)	T _J =25°C	I _R	-	5	50	uAmp
	T _J =100°C		-	3.5	10	mAmp
Thermal resistance from junction to case		R _{ΘJC}	10			°C/W
Operating Temperature Range		T _J	-65 to +175			°C
Storage Temperature Range		T _{STG}	-65 to +175			°C

NOTES:

1. Pulse test: 300 μs pulse width, 1% duty cycle
2. Pulse test: Pulse width ≤ 40 ms

RATINGS AND CHARACTERISTIC CURVES

