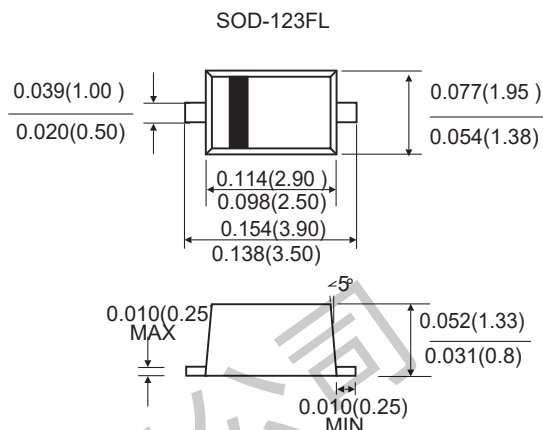


FEATURES

- For surface mounted applications
- Low profile package
- Built-in strain relief
- Easy pick and place
- Fast Recovery times for high efficiency
- Plastic package has Underwriters Laboratory
- Flammability Classification 94V-O
- High temperature soldering : 260°C /10 seconds at terminals

MECHANICAL DATA

- Case: Molded plastic, SOD-123FL
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number



Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, Resistive or inductive load. For capacitive load, derate current by 20%

	TEST CONDITION	SYMBOLS	VALUE			UNITS
			Min.	Typ.	Max.	
Breakover voltage ¹⁾	C=22nF ²⁾	V _{Bo}	28	32	36	VOLTS
Breakover voltage symmetry	C=22nF **	I+V _{Bo} I-I-V _{Bo} I	-3		3	VOLTS
Dynamic breakover voltage ¹⁾	(NOTE 1)	ΔV ± I	5			VOLTS
Output voltage ¹⁾	DIAGRAM2	V _o	5			VOLTS
Breakover current ¹⁾	C=22nF ²⁾	I _{Bo}			100	μA
Rise time ¹⁾	DIAGRAM3	t _r		1.5		μS
Leakage current ¹⁾	V _R =0.5V _{Bo}	I _B			10	μA
Power dissipation on printed circuit	T _A =65°C	P _d			150	mW
Repetitive peak on-state current	t _p =20μs f=100Hz	I _{TRM}			2	A
Thermal Resistances from Junction to ambient		R _{θJA}			400	°C/W
Thermal Resistances from Junction to lead		R _{θJL}			150	°C/W
Operating junction and storage temperature range		T _J , T _{STG}	-40		125	°C

1) :Electrical characteristic appoicaboe in forward and reverse directions.

2) :Connected in parallel with the devices.

Note 1:I_{Bo} from I_{Bo} to 10mA

Ordering Information (Example)

Preferred P/N	Package code	Unit weight(g)	Minimum Package(pcs)	Inner box Quantity(pcs)	Outer carton quantity(pcs)	Delivery mode
SOD-123	F1	Approximate 0.0169	3000	30000	240000	7" reel
SOD-123F	F2	Approximate 0.0169	10000	30000	150000	7" reel
SOD-123L						

The cruve graph is for reference only, can't be the basis for judgment

DIAGRAM 1:CURRENT-VOLTAGE CHARACTERISTICS

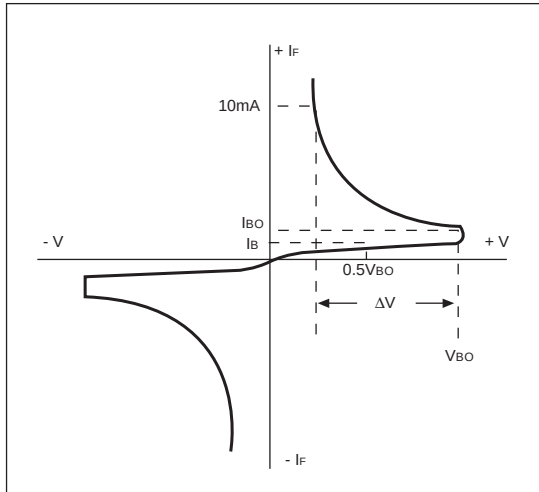


FIG. 1-POWER DISSIPATION VERSUS AMBIENT TEMPERATURE (MAXIMUM VALUES)

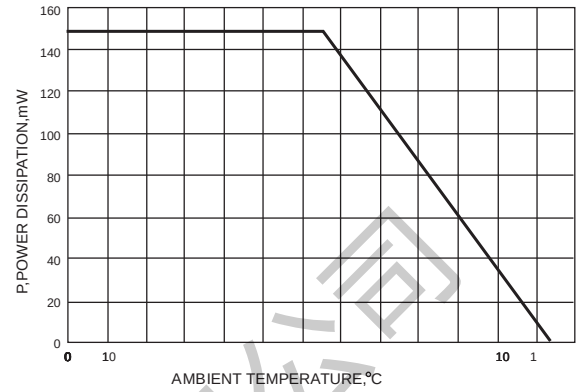


FIG. 2-PEAK PULSE CURRENT VERSUS PULSE DURATION (MAXIMUM VALUES)

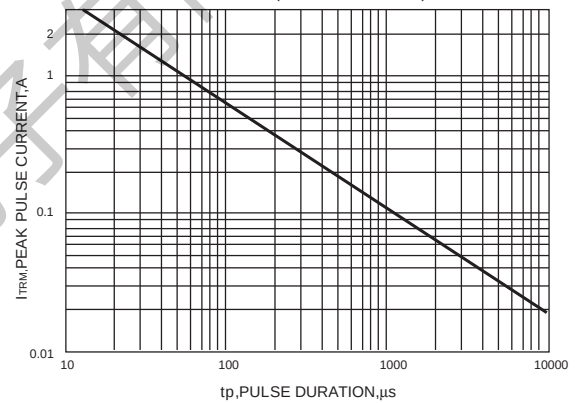


DIAGRAM 2:TEST CIRCUIT OUTPUT VOLTAGE

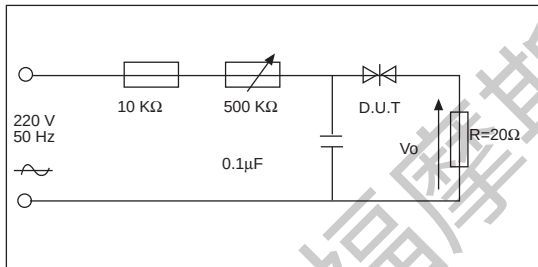


DIAGRAM 3:TEST CIRCUIT SEE DIAGRAM 2.ADJUST R FOR $I_P=0.5A$

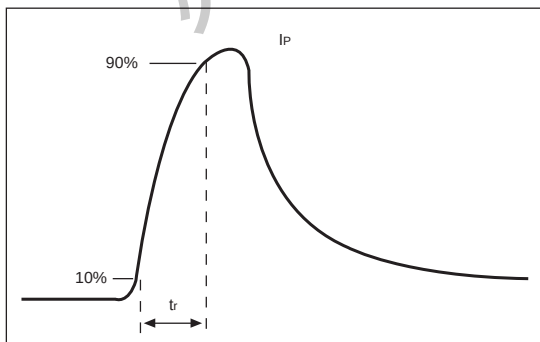


FIG. 3-RELATIVE VARIATION OF V_{BO} VERSUS JUNCTION TEMPERATURE (TYPICAL VALUES)

